

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
Timmins et al.

Serial No: N/A

Filed: Herewith

For: INFORMATION HANDLING SYSTEM
INCLUDING A BUS IN WHICH
IMPEDANCE DISCONTINUITIES
ASSOCIATED WITH MULTIPLE
EXPANSION CONNECTORS ARE REDUCED

Group Art Unit: N/A

Examiner: N/A

INFORMATION DISCLOSURE STATEMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In compliance with the duty of disclosure under 37 CFR §1.56, and in accordance with the practice under 37 CFR §1.97 and §1.98, the Examiner's attention is directed to the documents listed on the enclosed modified Form PTO-1449. No inference should be made that the cited references are in fact material, are in fact prior art, or that no better art exists. The cited patents are listed in numerical order and are not in any order based on their pertinence. Copies of the listed information are attached.

This Information Disclosure Statement is being filed within three months of the United States filing date or before the mailing date of a first Office Action on the merits. No certification or fee is required (37 CFR §1.97(b)).

It is respectfully requested that the above information be considered by the Examiner and that a copy of the enclosed Form PTO-1449 be returned indicating that such information has been considered.

Respectfully submitted,

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DATE OF DEPOSIT: 7/3/03

This paper and fee are being deposited with the U. S. Postal Service Express Mail Post Office to Addressee service under 37 CFR § 1.10 on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Nishi Pasarya

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Application Number	N/A
Filing Date	Herewith
Applicant(s)	Timmins et al.
Art Unit	N/A
Examiner Name	N/A
Attorney Docket Number	16356.793 (DC-03912)

SHEET 1 OF 1

U. S. PATENT DOCUMENTS

Examiner's Initials	Cite No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
	AA	5,406,453	04/11/1995	Cusato et al.
	AB	5,513,329	04/30/1996	Pecone
	AC	5,544,006	08/06/1996	Radloff et al.
	AD	5,594,621	01/14/1997	van Rumpt
	AE	5,604,871	02/18/1997	Pecone
	AF	5,655,106	08/05/1997	Smith
	AG	5,708,563	01/13/1998	Cranston, III et al.
	AH	5,734,840	03/31/1998	Chew et al.
	AI	5,765,008	06/09/1998	Desai et al.
	AJ	5,790,814	08/04/1998	Gan et al.
	AK	5,823,818	10/20/1998	Bell et al.
	AL	5,831,821	11/03/1998	Scholder et al.
	AM	5,878,238	03/02/1999	Gan et al.
	AN	5,926,378	07/20/1999	DeWitt et al.
	AO	5,991,158	11/23/1999	Chan et al.
	AP	6,118,668	09/12/2000	Scholder et al.
	AQ	6,288,898	09/11/2001	Johnson et al.
	AR	6,292,361	09/18/2001	Johnson et al.
	AS	6,297,957	10/02/2001	Johnson et al.
	AT	6,363,450	03/26/2002	Lash et al.

FOREIGN PATENT DOCUMENTS

Examiner's Initials	Cite No.	Foreign Patent Document (Country Code - Number - Kind)	Publication Date MM-DD-YYYY	Patentee or Applicant of Cited Document	Translation Y/N

OTHER PRIOR ART

Examiner's Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item, date, page(s), volume- issue number(s), publisher, city/country where published
	AU	ATX Riser Card Specification, Version 1.0, December 1999, Intel Corporation
	AV	High-Performance Buses and Interconnects, http://www.extremetech.com
	AW	BRENMAN, Leor, SmartLink--Using Riser Cards Can Shrink PC Connectivity Costs
	AX	TMC Product Riser Card, http://www.tmc-uk.com

Examiner
SignatureDate
Considered

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.